

Appendix 13 Bubble memory initialize

1 Introduction

When one of the following alarms occurs in FANUC SYSTEM 6, it suggests that a great error has occurred. The bubble memory must be initialized according to the following operational procedure.

Number	Contents
901	No Marker error occurs when power is turned on.
905	No Marker error occurs.
906	Many Defect Loop error occurs.

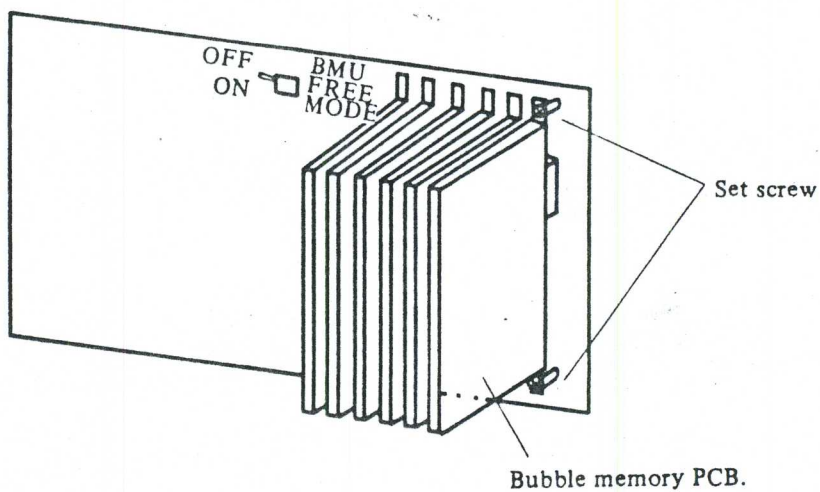
Note: Bubble memory initialize is to clear all contents of a bubble memory and rewrite data of the marker bit, defect loop, etc. in the bubble memory.

2 Operational procedure

(1) **Record defect loop data.**

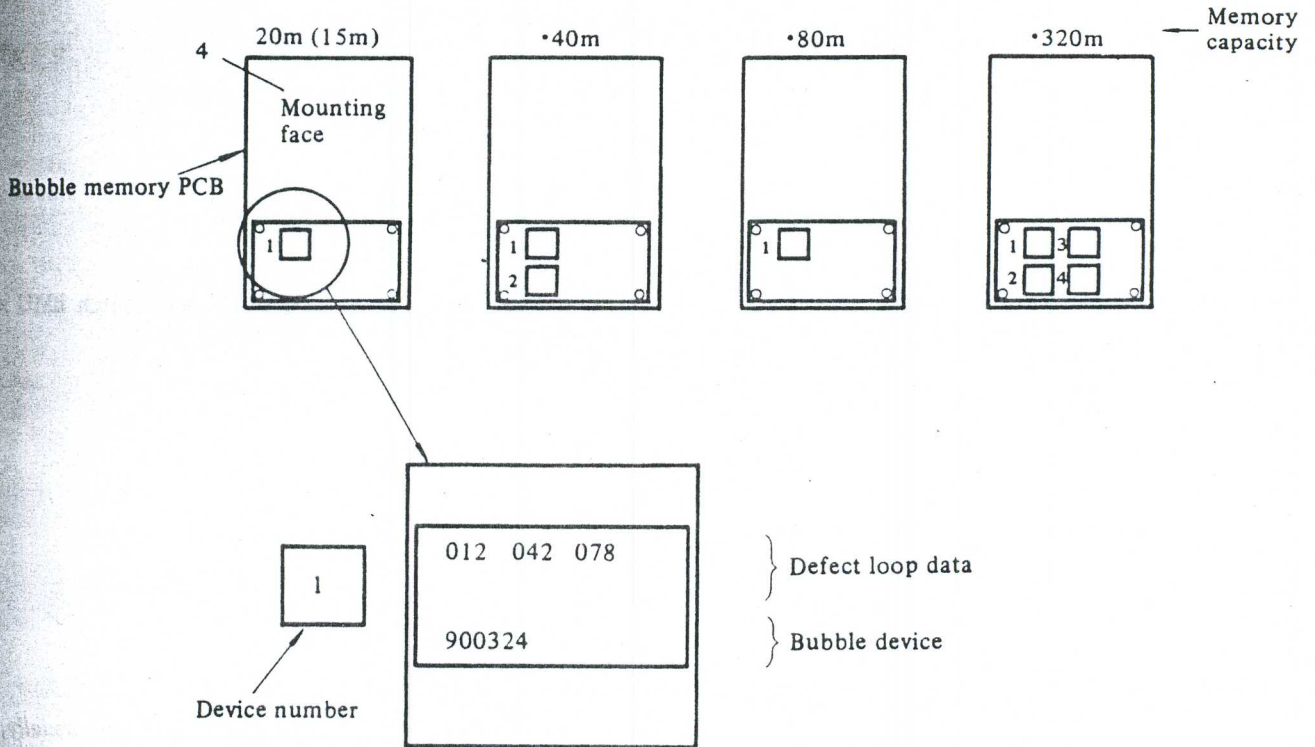
Defect loop data of the bubble memory is indicated on its PCB. Read this according to the following procedure.

- i) Power off the NC.
- ii) Remove the bubble memory PCB from the NC master PCB.



iii) Read defect loop data indicated on the bubble memory PCB.

a) Defect loop data is indicated in the following location.



(Note 1) The number of defect loops is indefinite and differs from device to device.

(Note 2) The number of devices differs as below, according to the memory capacity.

Memory capacity	Device number
20m (15m)	1
40m	2
80m	1
320m	4

(2) Bubble initialize according to the following procedure, using the MDI & CRT unit.

- i) Mount the bubble memory PCB on the master PCB, with power OFF.
- ii) Power ON while pressing buttons and . Then the following screen will be displayed.

```

IL-MODE
1. TAPE
2. MEMORY
3. ENPANE
4. BUBBLE
5. PC-LOAD
6. RAM TEST
    
```

iii) Press button **4** . Then the following screen will be displayed.

```
BUBBLE INITIALIZE
*FUNCTION KEY
1: WRITE BY TAPE
2: WRITE BY MANUAL
3: DISPLAY LOOP-DATA
ORIGIN: RETURN TO IL-MODE
```

iv) Press button **2** . Then the following screen will be displayed. (Proceed to v) when switch BMU is ON.)

```
BUBBLE INITIALIZE
MAKE BMU-SWITCH ON
```

(Note 1) When button **1** is pressed in screen iii), bubble initialize can be performed by tape. But usually, perform it on the MDI & CRT unit.

(Note 2) When button **3** is pressed in screen iii) in a state no bubble-associated alarm has occurred, the screen proceeds to iv). Set switch BMU ON. Then the bubble defect loop will be displayed on the screen ((screen vi) is displayed.)

v) Set the master PCB switch BMU ON. Then the following screen will be displayed.

```
BUBBLE INITIALIZE
DEVICE 1

INPUT =

INPUT: INPUT LOOP DATA
DELET: CLEAR ALL DATA
START: WRITE BUBBLE
```

vi) Key in defect loop data of DEVICE1 by DATA keys and press button **INPUT** . Repeat the above operation for two or more of defect loop data.

(When keyed-in defect loop data has an error, press button **DELET**. Then all keyed-in defect loop data will be cleared. After that, enter it again.)

After defect loop data of DEVICE1 has been entered, press button **START**. Then defect loop data will be written in DEVICE1 (taking tens of seconds).

When the data is not written correctly, the following screen is displayed.

